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LTR	DESCRIPTION	DR	CHK	APPR'D	DATE
A	RELEASE	MP	10-7-87		10/8/87
B	INC. ADCN A1 PER ECP	W	11-1-87		11/14/87

- NOTES: UNLESS OTHERWISE SPECIFIED.
- ALL RESISTOR NETWORKS ARE IN OHMS. 2K, 1/8W
 - ALL CAPACITORS ARE IN MICROFARADS. 50V.
 - LAST REFERENCE DESIGNATORS USED:
R27, C71, D3, CR72, U30, RP36, DP21, J1
TESTS, D510, SW2, K14, JP4
 - COMPONENTS OMITTED:
JP1, JP2, JP3, JP4
 - POWER AND GROUND TABLE:
- | DESIGNATOR | +5V | GND | +12V |
|---|----------------|-------------------|------|
| U4, 5, 6, 7, 10, 11, 12, 13, 17, 18, 19, 20 | 17, 34, 51, 68 | 1, 18, 27, 35, 52 | |
| U3, 16 | 10 | 9 | |
| U23 | | 12 | 3 |
| U24 | | 3 | 12 |
| U25, 26, 28, 29, 30 | 16 | 8 | |
| U27 | 14 | 7 | |
6. TRANSISTOR PINOUT FOR Q1, Q2, Q3 IS AS FOLLOWS:
- | DESIGNATOR | 1 | 2 | 3 |
|------------|---|---|---|
| Q1 | 1 | 2 | 3 |
| Q2 | 1 | 2 | 3 |
| Q3 | 1 | 2 | 3 |

P/N 700-2016-001

DRAWN M. PATTEE 10/7/87	DATE 10/7/87	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES.	DATA I/O REDMOND, WASH.
CHECKED B. Wallace 11/14/87	DATE 11/14/87	TOLERANCES, UNLESS OTHERWISE SPECIFIED: XX ± XXX ± ANGULAR	
MECH. ENG S. Ruppel 11/14/87	DATE 11/14/87	DO NOT SCALE DRAWING	TITLE SCHEMATIC DIAGRAM, SETSITE
QUAL ASSUR D. L. L. L. 11/14/87	DATE 11/14/87		SIZE D
APPD D. L. L. L. 11/14/87	DATE 11/14/87		FSCM NO. 54193
			DRAWING NO. 30-700-2016
			SCALE NONE
			D.A.D. SHEET 1 OF 2

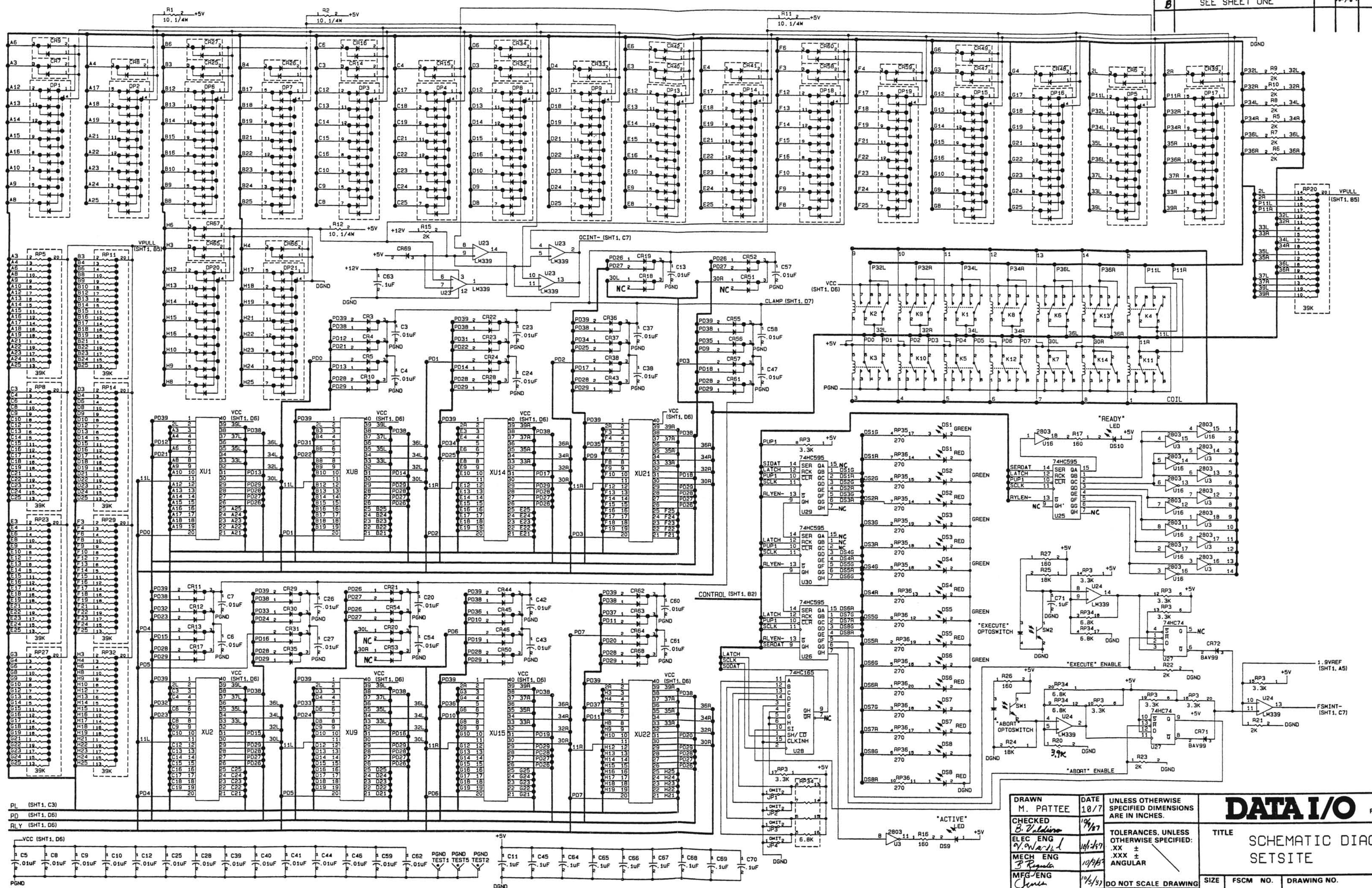
REVISIONS				
LTR	DESCRIPTION	DR	CHK	APPR'D DATE
B	SEE SHEET ONE	8/1/87		

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DRAWN M. PATTEE		DATE 10/7		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES.		DATA I/O REDMOND, WASH.					
CHECKED B. Valdivia		10/15/87		TOLERANCES, UNLESS OTHERWISE SPECIFIED: .XX ± .XXX ± ANGULAR		TITLE SCHEMATIC DIAGRAM, SETSITE					
ELEC ENG B. Valdivia		10/15/87									
MECH ENG B. Valdivia		10/15/87									
MFG ENG B. Valdivia		10/15/87									
QUAL ASSUR B. Valdivia		10/15/87		DO NOT SCALE DRAWING		SIZE D		FSCM NO. 54193		DRAWING NO. 30-700-2016	
APPR'D B. Valdivia		10/15/87				SCALE NONE		D.A.D.		SHEET 2 OF 2	