BOARDS

PIN	SIGNAL		CPU	MPX	ADDR	MEM	OUT	LED
1	A 13		●	○	+	○	○	●
2	*		*	*	*	*	*	*
3	GND		●	●	●	●	●	●
4	SL0		+	●	○	○	○	○
5	SL1		+	●	○	○	○	○
6	+5		●	●	●	●	●	●
7	−9		●	○	○	●	○	○
8	RDY		●	○	+	○	○	○
9	0	} DATA IN	●	+	+	●	●	●
10	1		●	+	+	●	●	●
11	2		●	+	+	●	●	●
12	3		●	+	+	●	●	●
13	4		●	+	+	●	●	●
14	5		●	+	+	●	●	●
15	6		●	+	+	●	●	●
16	7		●	+	+	●	●	●
17	*		*	*	*	*	+	*
18	7	} DATA OUT	+	○	●	●	○	○
19	6		+	○	●	●	○	○
20	5		+	○	●	●	○	○
21	4		+	○	●	●	○	○
22	3		+	○	●	●	○	○
23	2		+	○	●	●	○	○
24	1		+	○	●	●	○	○
25	0		+	○	●	●	○	○
26	R/W		+	○	○	●	○	○
27	$\overline{IN}$		+	●	○	○	○	○
28	$\overline{OUT}$		+	○	○	○	●	●
29	CC1		●	○	+	○	○	○
30	CC0		●	○	+	○	○	○
31	LAL		+	○	●	○	○	○
32	LAH		+	○	●	○	○	○
33	A 12		●	○	+	○	○	●
34	DEN		+	●	○	○	●	●
35	INT		●	○	+	○	○	○
36	INT		●	○	+	○	○	○
37	FLAG		*	*	*	*	*	*
38	A		●	○	+	●	○	○
39	B		●	●	+	●	●	●
40	C		●	●	+	●	●	●
41	D		●	●	+	●	●	●

NOTE 1 (spanning MPX pins 9–16)
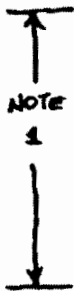
* = OPTIONAL — NOT USED
+ = SIGNAL SOURCE
○ = NOT USED / MAY BE SOLDERED TO THRU WIRE (NOTE 2)
● = USED / MUST " " " " " "

NOTES: 1) PARALLEL WIRES DO NOT GO ACROSS THIS AREA.
2) SHOULD BE WIRED THRU FOR BOARD MECHANICAL STABILITY.