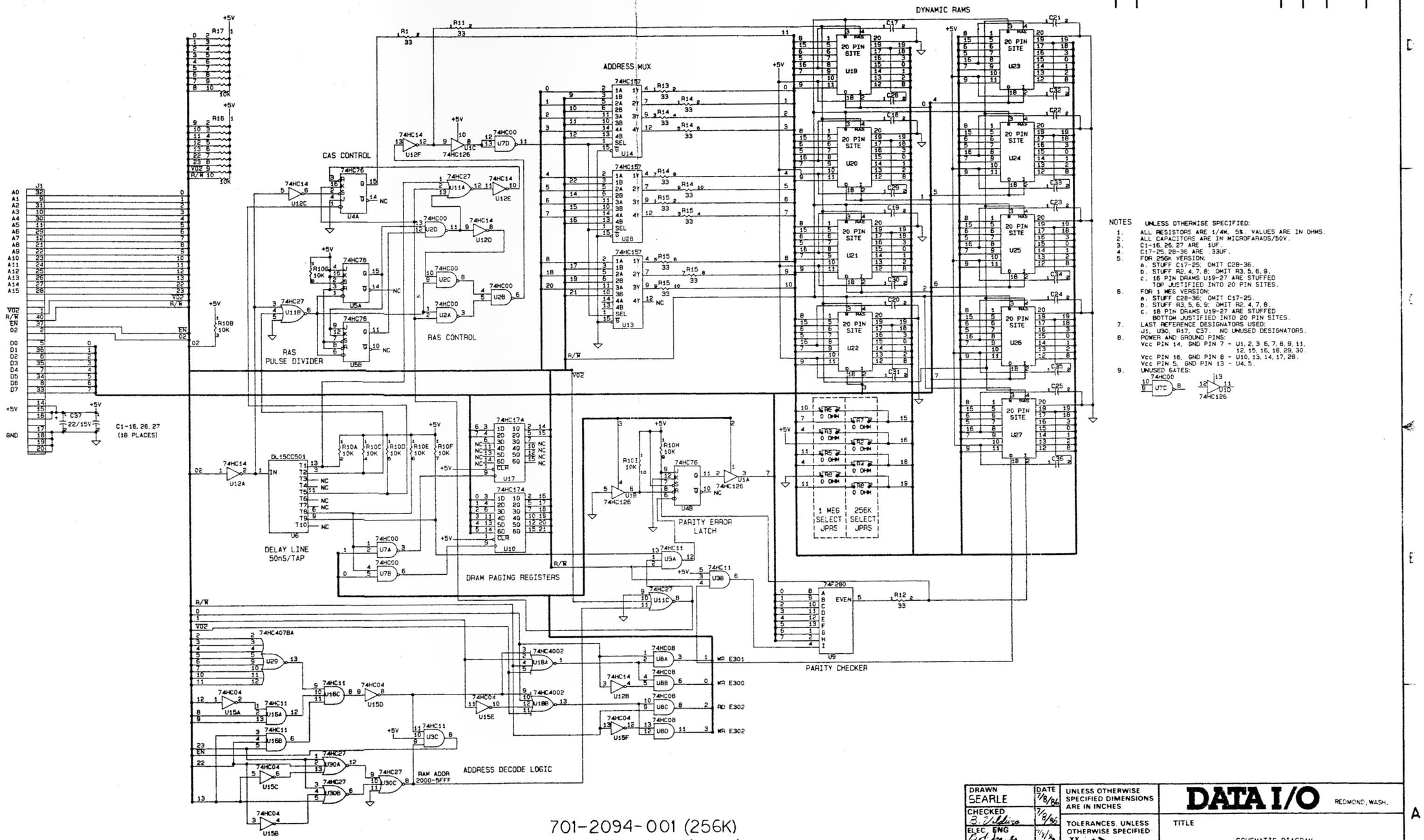
REVISIONS					
LTR	DESCRIPTION	DR	CHK	APPR'D	DATE
A	RELEASE	B.S.	B.V. 7-8-86	RJS	7/7/86



701-2094-001 (256K)
701-2094-002 (1 MEG)

NOTES UNLESS OTHERWISE SPECIFIED:

1. ALL RESISTORS ARE 1/4W, 5%. VALUES ARE IN OHMS.
2. ALL CAPACITORS ARE IN MICROFARADS/50V.
3. C1-16, 26, 27 ARE .1UF.
4. C17-25, 28-36 ARE .33UF.
5. FOR 256K VERSION:
   a. STUFF C17-25; OMIT C28-36.
   b. STUFF R2, 4, 7, 8; OMIT R3, 5, 6, 9.
   c. 16 PIN DRAMS U19-27 ARE STUFFED TOP JUSTIFIED INTO 20 PIN SITES.
6. FOR 1 MEG VERSION:
   a. STUFF C28-36; OMIT C17-25.
   b. STUFF R3, 5, 6, 9; OMIT R2, 4, 7, 8.
   c. 18 PIN DRAMS U19-27 ARE STUFFED BOTTOM JUSTIFIED INTO 20 PIN SITES.
7. LAST REFERENCE DESIGNATORS USED: J1, U30, R17, C37. NO UNUSED DESIGNATORS.
8. POWER AND GROUND PINS:
   Vcc PIN 14, GND PIN 7 - U1, 2, 3, 6, 7, 8, 9, 11, 12, 15, 16, 18, 29, 30.
   Vcc PIN 16, GND PIN 8 - U10, 13, 14, 17, 28.
   Vcc PIN 5, GND PIN 13 - U4, 5.
9. UNUSED GATES:
   74HC00 U7C
   74HC126 U1C, U10

DRAWN SEARLE	DATE 7/8/86	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	DATA I/O REDMOND, WASH.
CHECKED B. Valdivia	7/8/86	TOLERANCES UNLESS OTHERWISE SPECIFIED .XX ± .XXX ± ANGULAR	TITLE SCHEMATIC DIAGRAM M29B 256KB/1MB RAM BOARD
ELEC. ENG	7/1/86		
MECH. ENG B. Valdivia	7/8/86		
MFG ENG	7/1/86	DO NOT SCALE DRAWING	SIZE D · FSCM NO 54193 · DRAWING NO 30-701-2094
QUAL ASSUR			SCALE NONE · SHEET 1 OF 1
APPD	7/86		