

303A-011A/B
CMOS/BIPOLAR P/T ADAPTER

Acceptance Test Procedure

Data I/O Corporation warrants to the original purchaser of the product described by this manual that the product was fully functional to the extent of its specification at the time of shipment from the factory. Data I/O further certifies that the equipment used to test the product was calibrated to standards that are traceable to the National Bureau of Standards, as appropriate. Data I/O has made every attempt to ensure that the information in this document is accurate and complete. However, Data I/O assumes no liability for errors, or for any damages that result from use of this document or the equipment that it accompanies.

This acceptance test procedure is for use with the LogicPak and the following P/T adapters:

303A-001	IFL P/T Adapter
303A-007	Harris CMOS P/T Adapter
303A-008A	32R16 P/T Adapter (DIP + LCC)
303A-008B	32R16 P/T Adapter (DIP + NLCC)
303A-010	CMOS P/T Adapter
303A-011A	CMOS Bipolar P/T Adapter
303A-011B	CMOS Bipolar P/T Adapter
303A-012	AIM P/T Adapter

INTRODUCTION

This procedure is provided for customers whose company policy requires that a receiving inspection test be performed before the unit may be accepted.

Your LogicPak and/or P/T adapter was tested both electrically and mechanically before it was shipped, and was carefully packaged to prevent shipping damage. It should arrive free of any defect, without marks or scratches, and in perfect operating condition. However, carefully inspect the instrument for any damage that may have occurred in transit. If you note any damage, file a claim with the carrier and notify Data I/O at the nearest Customer Support office listed at the front of this User Note.

The LogicPak performance check consists of a brief self-check procedure to check the supply of current/voltages to the socket pins to verify proper operation.

Before beginning this test procedure, you must have the following equipment:

- LogicPak
- P/T adapter (see the Compatibility Table)
- Data I/O Programmer (see the Compatibility Table)

The following table lists applicable programmers and the minimum RAM required for each P/T adapter.

Compatibility Table

P/T Adapter	Programmer	Minimum Programmer RAM
303A-001	29A, 29B, System 19, Model 100A	8K
303A-007	29A, 29B, System 19, Model 100A	8K
303A-008A/B	29A, 29B	64K
303A-010	29A, 29B	64K
303A-011A	29A, 29B, System 19	16K
303A-011B	29A, 29B, System 19	16K
303A-012	29A, 29B, System 19	16K

Check for additional programmer compatibility requirements in the LogicPak Operator's Manual and your P/T adapter User Note.

Table 1-1. Test Procedure. LogicPak 303A with Any P/T Adapter*

For proper setup and connections refer to either the LogicPak Operator's Manual, the P/T adapter User Note, or the manual supplied with your programmer.

Step	Test Name Key Selection/ Procedure	Programmer Display	Programmer	Test Performed/ Comments
NA	Power-up test	Switch the power to the programmer ON and confirm that no error messages are displayed on the front panel.	If any errors occur, contact your nearest Data I/O Customer Support office.	When the system is powered-up, an internal self-test is performed. This test ensures that the firmware in the LogicPak is functioning correctly and that the various voltage supplies are also functioning correctly.

* Ensure that you are using one of the programmers listed for your P/T adapter (see the Compatibility Table).

Table 1-1. Test Procedure. LogicPak 303A with Any P/T Adapter* (cont.)

The following procedure takes the equipment through a series of self-tests to ensure the equipment is functioning properly. No readings or measurements are required. Possible errors for a given step are shown in the last column of this table.

Step	Test Name	Key Selection/ Procedure	Programmer Display	Test Performed/ Comments
NA	NA	SELECT	SELECT CODE ^	Selects the calibration mode.
		C 1	SELECT CODE ^ C 1	
1	All pins go to 0 volts	START	CAL STEP ^ 01	Checks supply and current voltages.
		START	CAL STEP ^ 01	
2	TTL driver self-check	START	CAL STEP ^ 02	TTL driver self-check. Possible errors are 9F-DF.
3	Calibration levels	START	CAL STEP ^ 03	Levels for calibration.
4	Source driver self-check	START	CAL STEP ^ 04	Source driver self-check. Possible errors are E0-FF.

NOTE

If any errors do occur, note the calibration step number in which the error was detected and the error code that was displayed. Then contact your nearest Data I/O Customer Support office or refer to the calibration procedure for the specific P/T adapter in Appendix B of the LogicPak Maintenance Manual.

If your acceptance requirements dictate a full calibration of the unit, contact Data I/O for the maintenance manual for the product.

ACCEPTANCE TEST PROCEDURE

This procedure, if performed with no detected errors, provides an adequate functional test of a new LogicPak and/or P/T adapter. Should programming yields become unacceptable, calibration of the unit may be required. For calibration, contact your nearest Data I/O Customer Support office.